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1-978-341-0242

T-407 P.03/04 F-947

Atturney's Docket No.: 2037.1006-012

Date of Allowance, February 8, 2006

March 3, 2006 MAR 0 2 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Valerie L. Lines

Application No.:

10/791,437

Group:

2824

Filed:

March 2, 2004

Examiner: Luu, Pho M.

Confirmation No.:

9944

For:

DYNAMIC MEMORY WORD LINE DRIVER SCHEME

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

A Notice of Allowance and Issue Fee Due was mailed for the above-captioned case on February 8, 2006. These comments are being submitted with the payment of the issue fee.

It is the claim as a whole that distinguishes the prior art. In particular, as noted by applicant in the Remarks of the Amendment dated November 17, 2005, claim 2 distinguishes the NOR gate implementation of patent 5,602,771 at least in that it recites crossed-coupled transistors connected drain-to-gate.

-2-

Authorization is hereby granted to charge any additional fees which may be due in this matter to Deposit Account No. 08-0380.

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

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FACSIMILE FILING OF PTO DOCUMENT

Examiner:

Luu, Pho M.

Group:

2824

Date:

March 2, 2006

Client Code:

2037

Facsimile No.:

Office of Patent Publication - 571-273-2885

From:

James M. Smith, Esq.

Subject:

Papers:

Issue Fee Transmittal and Comments on Statement of Reason for

Allowance

Docket No.:

2037.1006-012

Applicants:

Valerie L. Lines

Serial No.:

10/791,437

Filing Date:

March 2, 2004

Number of pages including this cover sheet: 4

Please confirm receipt of facsimile: Yes XX No ____

Comments:

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